

PATENT

F0660/AMDP660US

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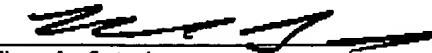
I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being faxed to 703-872-9306 on the date shown below to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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JUN 21 2005

Date: 6-21-05

Himanshu S. Amin

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:

Applicants(s): Bharath Rangarajan, *et al.*

Examiner: Richard A. Rosenberger

Serial No: 09/893,803

Art Unit: 2877

Filing Date: June 28, 2001

Title: SYSTEM AND METHOD FOR CREATION OF SEMICONDUCTOR MULTI-SLOPED FEATURES

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REPLY TO ADVISORY ACTION DATED JUNE 8, 2005

Dear Sir:

Favorable reconsideration of the above-identified patent application is respectfully requested in view of the following comments.